

Application No. 09/316,560

**REMARKS**

Claims 1, 4 and 5 have been amended. Claims 1-5 are currently pending in the application.

The Examiner rejected claims 1-5 as being unpatenable over Miller in view of Hubbins and Csoppenszky in view of Hubbins. Claim 5 was rejected as being anticipated by Torii. The claims have been amended to more clearly distinguish over the cited reference. Reconsideration is respectfully requested.

In particular, claims 1, 4 and 5 have been amended to recite that at least one of the first processor and the second processor accesses the memory circuits through a crossbar (Figure 1, elements 5 and 6). No corresponding arrangement is believed to be found in the cited references.

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Accordingly, claim 1, 4 and 5 are believed to patentably define over the cited reference. Claims 2 and 3 are also believed to add novel and patentable subject matter to independent claim 1.

Withdrawal of the rejection and allowance of claims 1-5 is respectfully requested.

Respectfully submitted,

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